

Application of Tai's Trial Function in an Improved Circuit Theory Two-Term Representation

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Abstract

A new trial function is used to derive the elements of the impedance matrix in the Improved Circuit Theory (ICT) for its application to multielement antennas. Results of the new ICT impedance are comparable in accuracy with the general method of moments. All the new functions are expressed in closed-form, and so still presents an ICT algorithm which is superior in terms of computer running time compared to method of moments.

1. Introduction

The Improved Circuit Theory (ICT) for Multielement Antennas uses a King-Middleton two-term trial function for the current distribution in an extended variational principle to derive the formula for the generalized impedance for multielement antennas of typical configuration as shown in Fig. 1 [1]. This represents a great improvement in the EMF theory evaluation of center-fed dipole antenna arrays. This is because the ICT method is able to take into adequate consideration the effects of mutual coupling without necessarily using an excessive number of current expansion functions as it is case with other methods like method of moments (MoM) [2]. The ICT method has also been implemented at much reduced CPU time by completely eliminating all time consuming numerical integration functions in its algorithm [3-4].

However, as we would show later, the ICT method is inadequate for much longer antenna lengths. Therefore for accurate evaluation of such antenna systems we are forced to use more conventional methods like MoM which whose application is not restricted to the antenna length but which is well known to be inherently time intensive and requires considerable amount of computer storage space.

In this paper, we present the results of the impedance of the ICT method as a result of a new choice in a trial function shown by C. T. Tai [5-6] to be far more superior in quality when used to derive the input impedance of center-fed dipole arrays. It is shown that the new impedance formula leads to the possibility of much extended application of the ICT method and also establishes the valid region of the conventional ICT method.

2. Tai's Trial Function in ICT Two-term Representation

In the classical King-Middleton ICT two-term representation, the generalized impedance matrix is derived with the with the two-term current functions

$$f_i^1(z_i) = \frac{\sin k(h - |z_i|)}{\sin kh} \quad (1)$$

$$f_i^2(z_j) = \frac{1 - \cos k(h - |z_j|)}{\cos kh} \quad (2)$$

But C. T. Tai has shown the these two trial functions are not sufficiently accurate for antenna lengths greater than one wavelength for the case of single elements [5-6]. To extend Tai's suggestion to multielement antennas we have also replaced second component of the trial function as follows

$$f_i^2(z_j) = \frac{k(h - |z_j|) \cos(h - |z_j|)}{kh \cos kh} \quad (3)$$

Eqs. 1 and 3 have therefore been used in a new two-term representation to derive the elements of the impedance matrix all expressible in closed-form has shown in the appendix.

3. Results and Discussion

We have validated the new ICT formula by comparing the input impedances computed with it to other conventional methods. Fig. 2 compares the admittances computed for different antenna lengths using C. T. Tai's trial function, King-Middleton ICT, C. T. Tai's single element variational [5] and MoM [2]. We can see the considerable agreement between our new formula and MoM in particular. The results also establishes the region of validity of the conventional ICT formula because as can be deduced from Figs. 2, it is inadequate for element lengths above 1.8λ .

The attractiveness of the ICT method is captured in Table 1 which compares a typical CPU time on an NEC PC-9801VX for the various methods.

4. Conclusion

We have shown that the use of C. T. Tai's trial function in a two-term ICT representation leads to an impedance formula which has the possibility of expanding the region of validity of the ICT method in the analysis of linear wire antennas. Since all components of the new formula are expressible in closed-form, ICT is still computationally efficient in terms of CPU time. We have also established the valid region of the conventional ICT method.

The results reported are applicable to only equal length center-fed dipole arrays, but the same procedure can be used to derive a more general formula for an array of center-fed dipoles of arbitrary length.

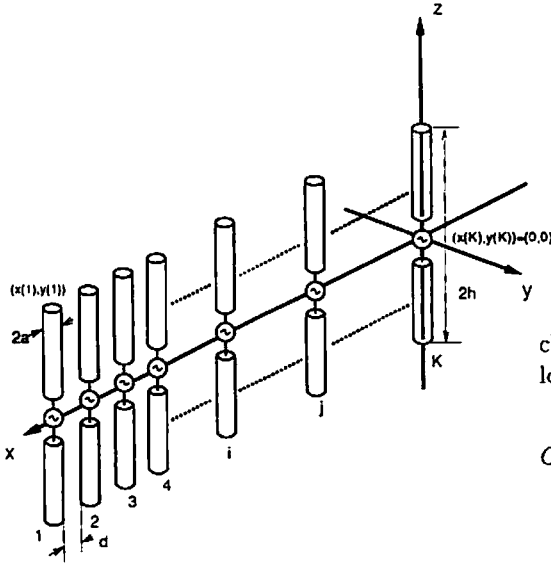


Figure 1: Array of Cylindrical Dipoles.

5. Appendix

Using the current functions in Eqs. 3 and 5 in a two-term representation, it can be shown that the elements of the impedance matrix in ICT can be expressed as

$$Z_{ij}^{11} = j30csc^2 kh \{ (2 + \cos 2kh) S_{kd}(kh) + \sin 2kh [C_{kd}(2kh) - 2C_{kd}(kh)] - \cos 2kh [S_{kd}(2kh) - S_{kd}(kh)] \} \quad (4)$$

$$Z_{12}^{12} = \frac{j60 csc 2kh}{kh} [2kh \cos 2kh C_{kd}(2kh) + 2kh \sin 2kh S_{kd}(2kh) - \cos 2kh CU_{1kd}(2kh) - \sin 2kh SU_{1kd}(2kh) + 2(1 + \cos 2kh) CU_{1kd}(kh) + 2 \sin 2kh SU_{1kd}(kh) - kh(1 + 3 \cos 2kh) C_{kd}(kh) - 3kh \sin 2kh S_{kd}(kh)] \quad (5)$$

$$Z_{12}^{22} = j \frac{30}{k^2 h^2 (1 + \cos 2kh)} \{ [-\sin 2kh CU_{2kd}(2kh) + \cos 2kh SU_{2kd}(2kh) + [4kh \sin 2kh - \cos 2kh] CU_{1kd}(2kh) + [4kh \sin 2kh - \cos 2kh] CU_{1kd}(2kh) - [4kh \cos 2kh$$

$$+ \sin 2kh] SU_{1kd}(2kh) + (1 - 4(kh)^2) \sin 2kh + 2kh \cos 2kh] C_{kd}(2kh) + [2kh \sin 2kh - (1 - 4(kh)^2) \cos 2kh] S_{kd}(2kh) + 2 \sin 2kh CU_{2kd}(kh) - 2 \cos 2kh SU_{2kd}(kh) + 2[1 + \cos 2kh - 4kh \sin 2kh] CU_{1kd}(kh) + 2[-kh + \sin 2kh + 4kh \cos 2kh] SU_{1kd}(kh) - 2 [kh + kh \cos 2kh - (3(kh)^2 - 1) \sin 2k] \cdot C_{kd}(kh) + 2[(kh)^2 + 1 - kh \sin 2kh - (3(kh)^2 - 1) \cos 2kh] S_{kd}(kh) \} \quad (6)$$

$C_D(x)$ and $S_D(x)$ are well documented closed-form functions defined in while the following are new functions [7]:

$$CU_{1D}(x) = \frac{1}{2} [-2(D+j)e^{-jD} + j(e^{-jv} + e^{-ju}) + D^2 \{ (\frac{e^{-jv}}{v} + \frac{e^{-ju}}{u}) + j[E_i[-jv] + E_i[-ju] - 2E_i[-jD]] \}] \quad (7)$$

$$SU_{1D}(x) = \frac{1}{2} [jD^2 (\frac{e^{-jv}}{v} - \frac{e^{-ju}}{u}) - (e^{-jv} - e^{-ju}) + D^2 [E_i[-jv] - E_i[-ju]]] \quad (8)$$

$$CU_{2D}(x) = \frac{1}{2} [(\frac{1}{2} + j\frac{v}{2} - (\frac{D^2}{2v})^2 + j\frac{D^4}{4v})e^{-jD} - (\frac{1}{2} + j\frac{u}{2} - (\frac{D^2}{2u})^2 + j\frac{D^4}{4u})e^{-ju} - D^2 [1 + (\frac{D}{2})^2] \cdot [E_i[-jv] - E_i[-ju]]] \quad (9)$$

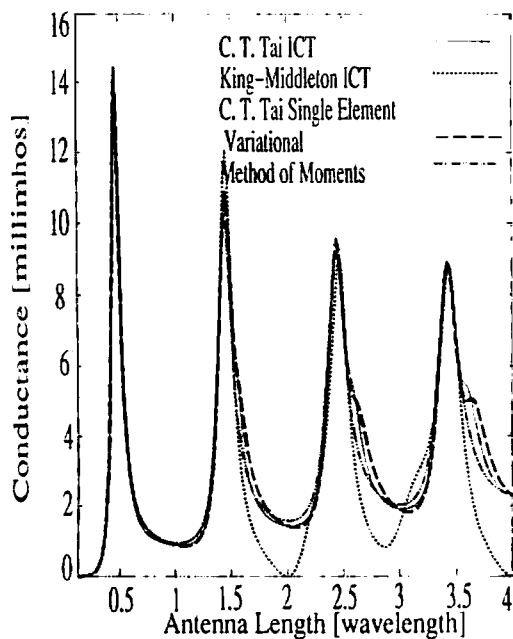
$$SU_{2D}(x) = \frac{j}{2} [(-1 - jD + \frac{D^2}{2} - j\frac{D^3}{2})e^{-jD} + (\frac{1}{2} + j\frac{v}{2} - (\frac{D^2}{2v})^2 + j\frac{D^4}{4v})e^{-jv} + (\frac{1}{2} + j\frac{u}{2} - (\frac{D^2}{2u})^2 + j\frac{D^4}{4u})e^{-ju} - D^2 [1 + (\frac{D}{2})^2] [E_i[-jv] + E_i[-ju] - 2E_i[-jD]]] \quad (10)$$

$$u = \sqrt{D^2 + x^2} - x$$

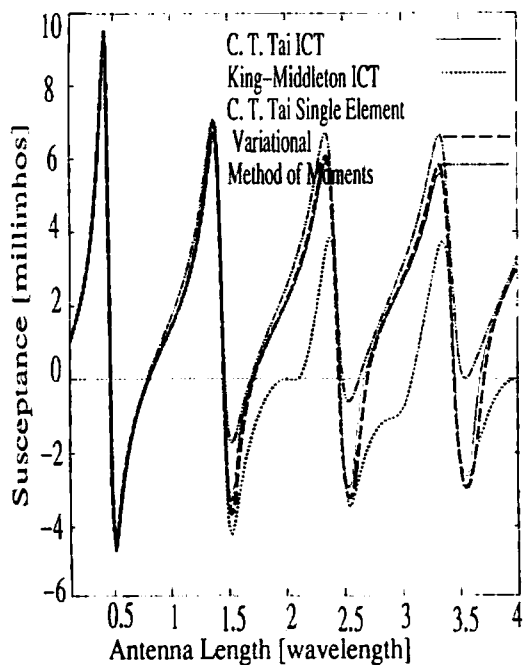
$$v = \sqrt{D^2 + x^2} + x \quad (11)$$

$$E_i[-jz] = C_i(z) - C_i(0) - jS_i(z) \quad (12)$$

$C_i(z)$ and $S_i(z)$ are the well known cosine and sine integral functions respectively.



(a)



(b)

Figure 2: Input Admittance for $\Omega = 10$
 (a) Conductance, (b) Susceptance.

Table 1: CPU Time on NEC PC-9801 VX.

Method	Time [seconds]
Tai ICT	0.11
King-Middleton ICT	0.27
Faster ICT [3]	0.07
Tai Variational EMF	0.03
Method of Moments	96

References

- [1] N. Inagaki : "An Improved Circuit Theory of Multielement Antenna", IEEE Transactions On Antennas and Propagation, vol. AP-17.No.2, pp. 120- 124, March 1969.
- [2] Warren L. Stutzman, Gary A. Thiele: "Antenna Theory and Design", John Willy & Sons, pp. 349-354, pp. 582-591, New York 1981.
- [3] A. I. Imoro, N. Inagaki and N. Kikuna, "A More Accurate and Compact Faster Improved Circuit Theory Algorithm". IEICE, Japan, March 1995, p. C29.
- [4] "A Faster Improved Circuit Theory Algorithm" *IEEE Antenna and Propagation International Symposium*, Seattle, Washington, USA Vol.3, pp. 780-782. June 1994.
- [5] Robert E. Collin and Francis J. Zucker. "Antenna Theory part 1". McGraw-Hill Book Company, 1969, pp. 55-57.
- [6] C. T. Tai. "A New interpretation of the Integral Equation Formulation of Cylindrical Antennas." IRE Trans. Antennas and Propagat., AP-3, (1955), pp. 125-127.
- [7] Stephen Wolfram: "MATHEMATICA A System for Doing Mathematics by Computer", Addison-Wesley Publishing Company, Inc, The Advanced Book Program, 2^{ed} Edition, 1991, pp. 672-679.

Trial and error is a fundamental method of problem-solving. It is characterized by repeated, varied attempts which are continued until success, or until the practitioner stops trying. According to W.H. Thorpe, the term was devised by C. Lloyd Morgan (1852–1936) after trying out similar phrases "trial and failure" and "trial and practice". Under Morgan's Canon, animal behaviour should be explained in the simplest possible way. Where behavior seems to imply higher mental processes, it might be explained by From Circuit Theory. L. A. ZADEH, Summary-The past two decades have witnessed profound changes in the composition, functions and the level of complexity of electrical as well as electronic systems which are employed in modern technology. I. INTRODUCTION The past two decades have witnessed an evolution of classical circuit theory into a field of science whose domain of application far transcends the analysis and synthesis of RLC networks. Among these, to mention a few, are his representation of nonlinear systems in terms of a series of Laguerre polynomials and Hermite functions, his theory of prediction and filtering, his generalized harmonic analysis, his cinemaintegraph, the Paley-Wiener criterion, and the Wiener process. Improving Discrete Latent Representations With Differentiable Approximation Bridges. Jason Ramapuram. Modern neural network training relies on piece-wise (sub-)differentiable functions in order to use backpropagation to update model parameters. In this work, we introduce a novel method to allow simple non-differentiable functions at intermediary layers of deep neural networks. makes it a strong candidate for applications requiring non-differentiable functions. Adding a learning rate scheduler to the DAB based training is likely to improve convergence time, however this is left to future work. 4.3. Image Classification. In steady state a.c. circuit theory, the ability of a circuit to accept a current flow resulting from a given driving voltage is called the impedance of the circuit. Since current and voltage are duals the impedance parameter must also have a dual, called admittance. 4.1 Circuit variables. As current and voltage are sinusoidal functions of time, varying at a single and constant frequency, they are regarded as rotating vectors and can be drawn as plan vectors (that is, vectors defined by two co-ordinates) on a vector diagram. Voltage rise is a rise in potential measured in the direction of current flow between two points in a circuit. Voltage drop is the converse. A circuit element with a voltage rise across it acts as a source of energy.